

**AMENDMENTS TO THE CLAIMS:**

1-14. (Cancelled)

15. (Amended) A liquid crystal display device comprising:

a substrate;

first and second gate lines arranged substantially in parallel above the substrate;

a bus line arranged to intersect the first and second gate lines to define a pixel;

a transistor having a source electrode and a drain electrode formed near an intersection part of the bus line and the first gate line, the source electrode being connected to the bus line;

at least one data electrode connected to the drain electrode of the transistor;

a passivation layer formed above the transistors and the at least one data electrode; and

at least one common electrode arranged above the passivation layer in parallel with the second gate line, the at least one common electrode and the at least one data electrode engaged in an in-plane switching mode, wherein ~~portions of at least two of the second gate line, the data electrode and the common electrode are~~ overlapping with each other the data electrode.

16. (Cancelled)

17. (Original) The liquid crystal display device of claim 15, wherein the second gate line has no overlapping portions with the data electrode and the common electrode.

18. (Original) The liquid crystal display device of claim 15, wherein the data electrode has no overlapping portions with the common electrode.

19. (Original) The liquid crystal display device of claim 15, wherein the second gate line has no overlapping portions with the common electrode.

20-21. (Cancelled)

22. (Original) The liquid crystal display device of claim 17, wherein the second gate bus line and the data electrode form a first storage capacitor.

23. (Original) The liquid crystal display device of claim 17, wherein the data electrode and the common electrode form a second storage capacitor.

24. (Original) The liquid crystal display device of claim 18, wherein the second gate bus line and the data electrode form a first storage capacitor.

25. (Original) The liquid crystal display device of claim 18, wherein the data electrode and the common electrode form a second storage capacitor.

26. (Original) The liquid crystal display device of claim 19, wherein the second gate bus line and the data electrode form a first storage capacitor.

27. (Original) The liquid crystal display device of claim 19, wherein the data electrode and the common electrode form a second storage capacitor.

28. (Original) The liquid crystal display device of claim 15, wherein a second substrate formed above the substrate;

a first alignment layer formed above the passivation layer; and

a second alignment layer formed on the second substrate.

29. (Original) The liquid crystal display device of claim 28, wherein the first alignment layer and the second alignment layer comprise one of polyamide, polyimide, SiO<sub>2</sub>, polyvinylalcohol, polyamic acid and a photosensitive material.

[[29]] 30. (Amended) The liquid crystal display device of claim 29 wherein the photosensitive material comprises one of polyvinylcinnamate, polysiloxanecinnamate and cellulosecinnamate.

[[30]] 31. (Amended) A method of manufacturing a liquid crystal display device comprising the steps of:

providing a substrate;

forming first and second gate lines arranged substantially in parallel above the substrate;

forming a bus lines to intersect the first and second gate lines to define a pixel;

fabricating a transistor having a source electrode and a drain electrode near an intersection part of the bus line and the first gate line, the source electrode being connected to the bus line;

forming at least one data electrode connected to the drain electrode of the transistor;

arranging a passivation layer above the transistors and the at least one data electrode; and

forming at least one common electrode above the passivation layer in parallel with the second gate line, the at least one common electrode and the at least one data electrode, wherein ~~portions of at least two of the second gate line, the data electrode and the common electrode are~~ is overlapping with ~~each other~~ the data electrode.

[[31]] 32. (Cancelled)

33. (New) The liquid crystal display device of claim 31, wherein the data electrode has no overlapping portions with the common electrode.

34. (New) The liquid crystal display device of claim 31, wherein the second gate line has no overlapping portions with the common electrode.

35. (New) The liquid crystal display device of claim 33, wherein the second gate bus line and the data electrode form a first storage capacitor.

36. (New) The liquid crystal display device of claim 33, wherein the data electrode and the common electrode form a second storage capacitor.

37. (New) The liquid crystal display device of claim 34, wherein the second gate bus line and the data electrode form a first storage capacitor.

38. (New) The liquid crystal display device of claim 34, wherein the data electrode and the common electrode form a second storage capacitor.